

a logic circuit responsive to said first output signal, the second external signal, and the third external signal for producing an output signal; and

a latch responsive to said output signal of said logic circuit for producing said second enable signal.

465. The device of claim 464, wherein said logic circuit includes:

a NAND gate having a first input terminal in communication with said first output signal, a second input terminal in communication with the second external signal, a third input terminal in communication with the third external signal, and an output terminal for producing said output signal of said logic circuit. --

REMARKS

The instant amendment presents new claims 81 – 465 for examination. No new matter has been added by claims 81 – 465.

STATEMENT REQUESTING DELETION OF INVENTORS

As a result of the restriction requirement, the following inventors named in the parent application are not inventors of the invention claimed in the instant divisional application:

Raymond J. Beffa
Frank K. Ross
Larry D. Kinsman
Ronald L. Taylor
John S. Mullin

Please file the instant application in the names of the remaining inventors (Keeth, Bunker and Derner) in accordance with 37 CFR 1.63 (d).

FIG. 108 is reproduction of FIG. 4f illustrating an array slice to be discussed in connection with the all row high test mode;

FIG. 109 is a reproduction of FIG. 6A with the sense
5 amps and the row decoders illustrated for purposes of explaining the all row high test mode;

FIG. 110 identifies various exemplary dimensions for the chip of the present invention;

FIG. 111 illustrates the bonding connections between
10 the chip and the lead frame;

FIG. 112 illustrates a substrate carrying a plurality of chips constructed according to the teachings of the present invention; and

FIG. 113 illustrates the DRAM of the present invention
15 used in a microprocessor based system.

Microfiche Appendix

Reference is hereby made to an appendix which contains [nine microfiche having a total of fifty-two frames] eleven
20 microfiche having a total of sixty-six frames. The appendix contains [33] 44 drawings which illustrate substantially the same information as is shown in FIGs. 1-113, but in a more connected format.